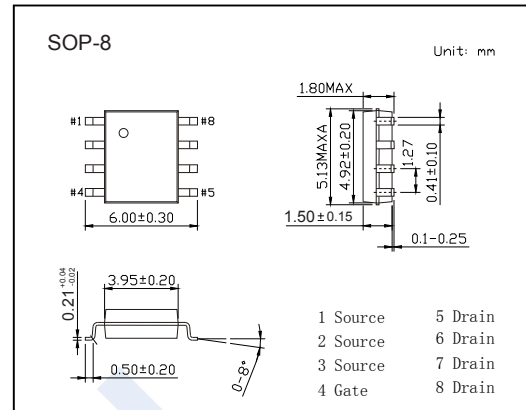
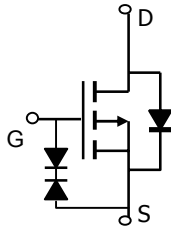


P-Channel MOSFET

AO4423 (KO4423)

■ Features

- $V_{DS} (V) = -30V$
- $I_D = -15 A (V_{GS} = -20V)$
- $R_{DS(ON)} < 7m\Omega (V_{GS} = -20V)$
- $R_{DS(ON)} < 8.5m\Omega (V_{GS} = -10V)$
- $R_{DS(ON)} < 12 m\Omega (V_{GS} = -6 V)$
- ESD Rating: 3000V HBM

■ Absolute Maximum Ratings $T_a = 25^\circ C$

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	V_{DS}	-30	V	
Gate-Source Voltage	V_{GS}	± 25		
Continuous Drain Current	I_D	$T_A = 25^\circ C$	-15	A
		$T_A = 70^\circ C$	-12.1	
Pulsed Drain Current	I_{DM}	-80		
Power Dissipation	P_D	$T_A = 25^\circ C$	3.1	W
		$T_A = 70^\circ C$	2	
Thermal Resistance.Junction- to-Ambient	R_{thJA}	$t \leq 10s$	40	$^\circ C/W$
		Steady-State	75	
Thermal Resistance.Junction- to-Lead	R_{thJL}	24		
Junction Temperature	T_J	150	$^\circ C$	
Junction Storage Temperature Range	T_{stg}	-55 to 150		

P-Channel MOSFET

AO4423 (KO4423)

■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Drain-Source Breakdown Voltage	V _{DSS}	I _D =-250 μA, V _{GS} =0V	-30			V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} =-24V, V _{GS} =0V			-100	nA
		V _{DS} =-24V, V _{GS} =0V, T _J =55°C			-500	
Gate-Body leakage current	I _{GSS}	V _{DS} =0V, V _{GS} =±20V			±1	μA
		V _{DS} =0V, V _{GS} =±25V			±10	
Gate Threshold Voltage	V _{GS(th)}	V _{DS} =V _{GS} , I _D =-250 μA	-2		-3.5	V
Static Drain-Source On-Resistance	R _{DS(on)}	V _{GS} =-20V, I _D =-15A			7	mΩ
		V _{GS} =-20V, I _D =-15A, T _J =125°C			8.6	
		V _{GS} =-10V, I _D =-15A			8.5	
		V _{GS} =-6V, I _D =-10A			12	
On state drain current	I _{D(ON)}	V _{GS} =-10V, V _{DS} =-5V	-80			A
Forward Transconductance	g _{FS}	V _{DS} =-5V, I _D =-15A		43		S
Input Capacitance	C _{iss}	V _{GS} =0V, V _{DS} =-15V, f=1MHz		4632		pF
Output Capacitance	C _{oss}			1034		
Reverse Transfer Capacitance	C _{rss}			705		
Gate resistance	R _g		V _{GS} =0V, V _{DS} =0V, f=1MHz		2.5	
Total Gate Charge	Q _g	V _{GS} =-10V, V _{DS} =-15V, I _D =-15A		82		nC
Gate Source Charge	Q _{gs}			16.8		
Gate Drain Charge	Q _{gd}			23		
Turn-On DelayTime	t _{d(on)}	V _{GS} =-10V, V _{DS} =-15V, R _L =1Ω, R _{GEN} =3Ω		18.5		ns
Turn-On Rise Time	t _r			20		
Turn-Off DelayTime	t _{d(off)}			55		
Turn-Off Fall Time	t _f			30		
Body Diode Reverse Recovery Time	t _{rr}	I _F =-15A, dI/dt=100A/μs		43		nC
Body Diode Reverse Recovery Charge	Q _{rr}			38		
Maximum Body-Diode Continuous Current	I _S				-4.2	A
Diode Forward Voltage	V _{SD}	I _S =-1A, V _{GS} =0V			-1	V

Note : The static characteristics in Figures 1 to 6 are obtained using <300 μs pulses, duty cycle 0.5% max.

■ Marking

Marking	4423
	KC****

P-Channel MOSFET AO4423 (KO4423)

■ Typical Characteristics

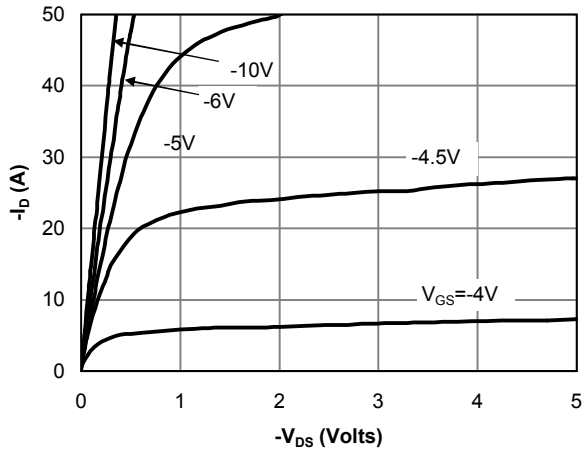


Fig 1: On-Region Characteristics

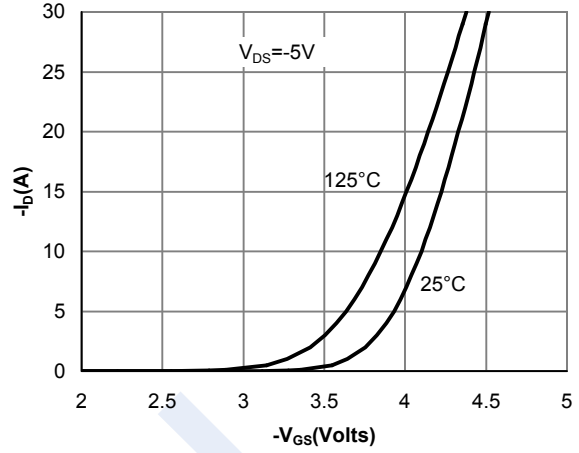


Figure 2: Transfer Characteristics

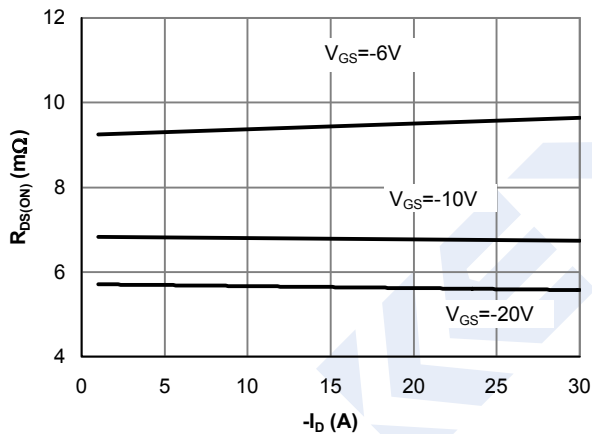


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

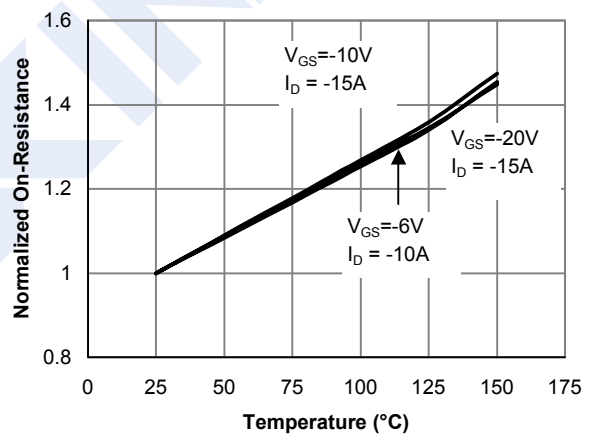


Figure 4: On-Resistance vs. Junction Temperature

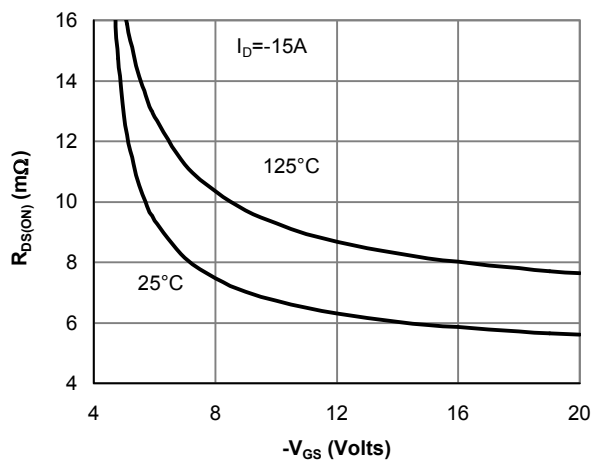


Figure 5: On-Resistance vs. Gate-Source Voltage

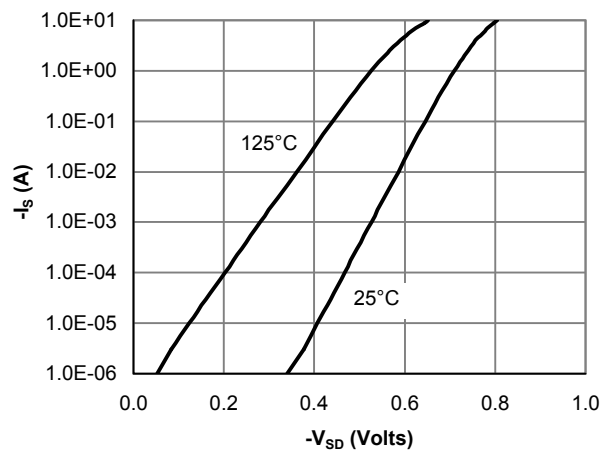


Figure 6: Body-Diode Characteristics

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■ Typical Characteristics

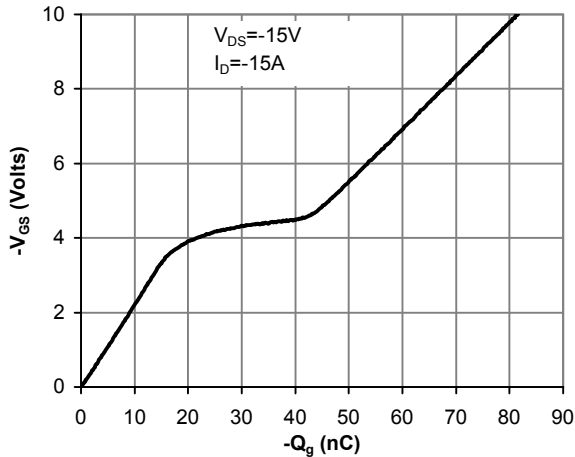


Figure 7: Gate-Charge Characteristics

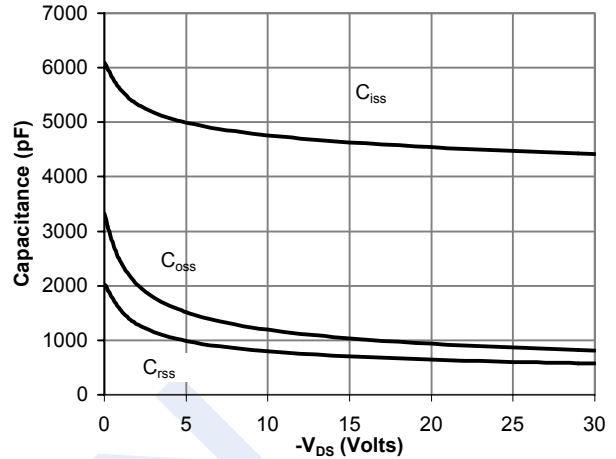


Figure 8: Capacitance Characteristics

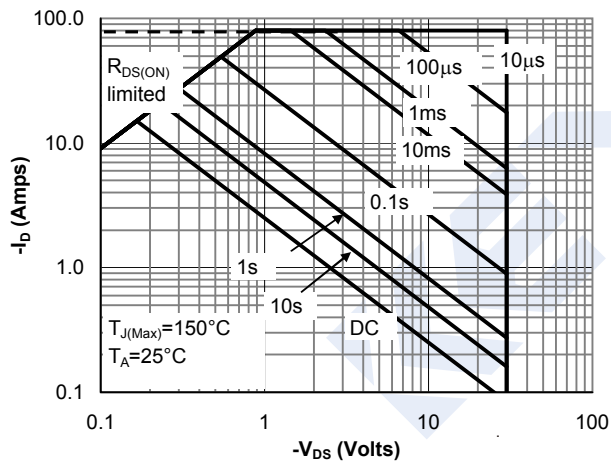


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

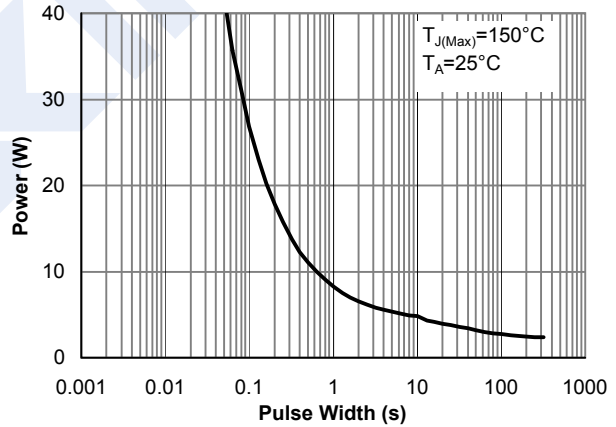


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

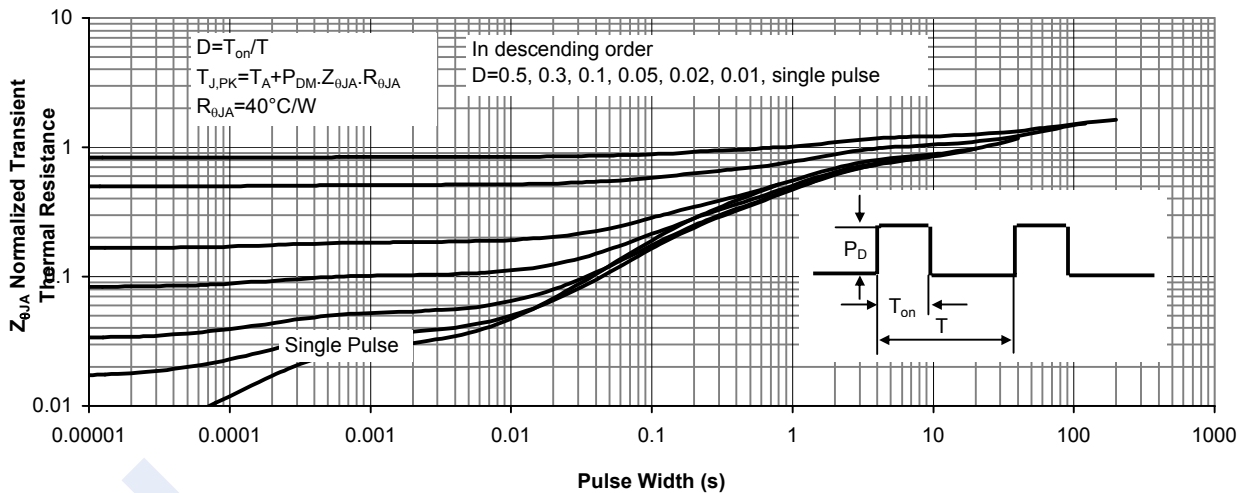


Figure 11: Normalized Maximum Transient Thermal Impedance